

FIGURE 1

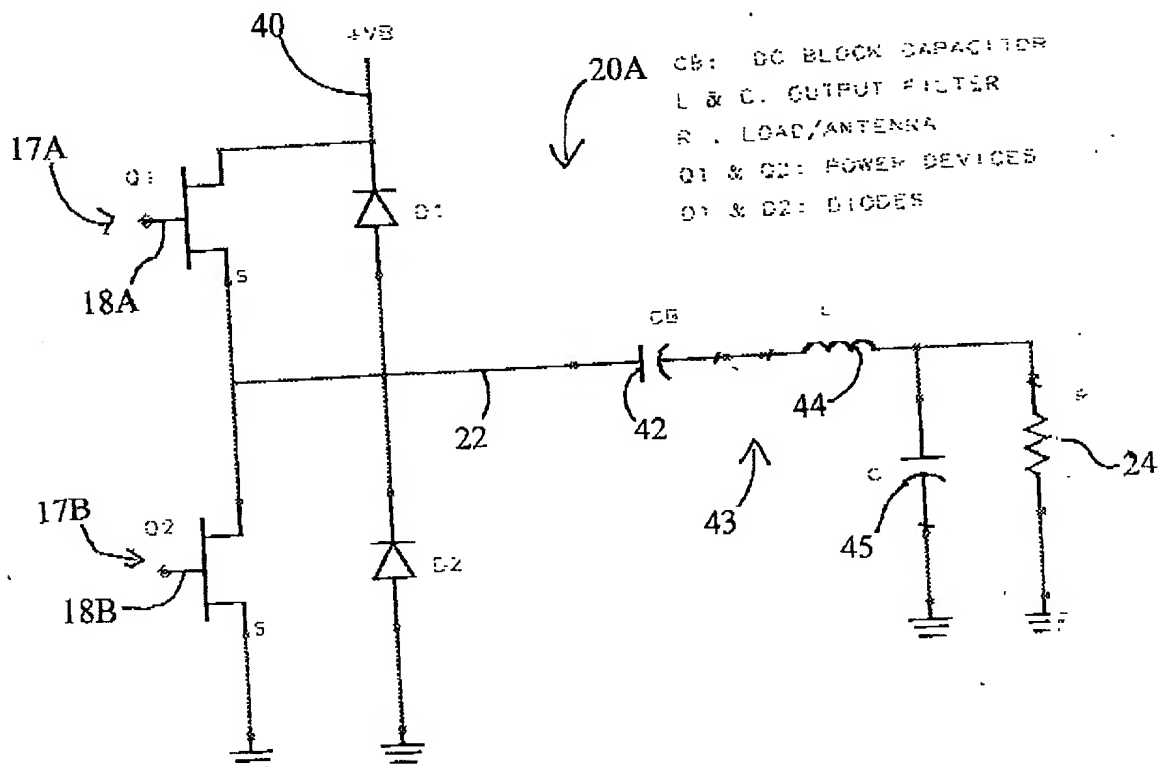


FIG. 2. CLASS S AMPLIFIER WITH 2 DEVICES.

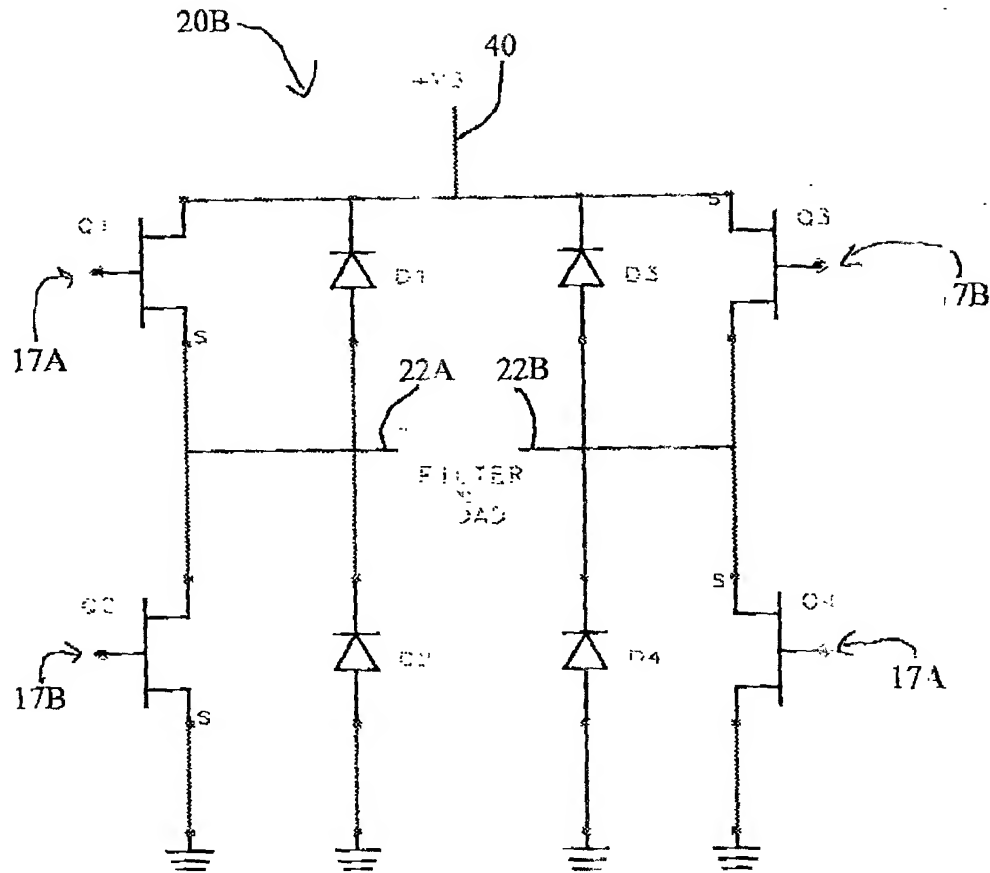


FIG. 3. CLASS S AMPLIFIER WITH 4 DEVICES. (H CONFIGURATION.)

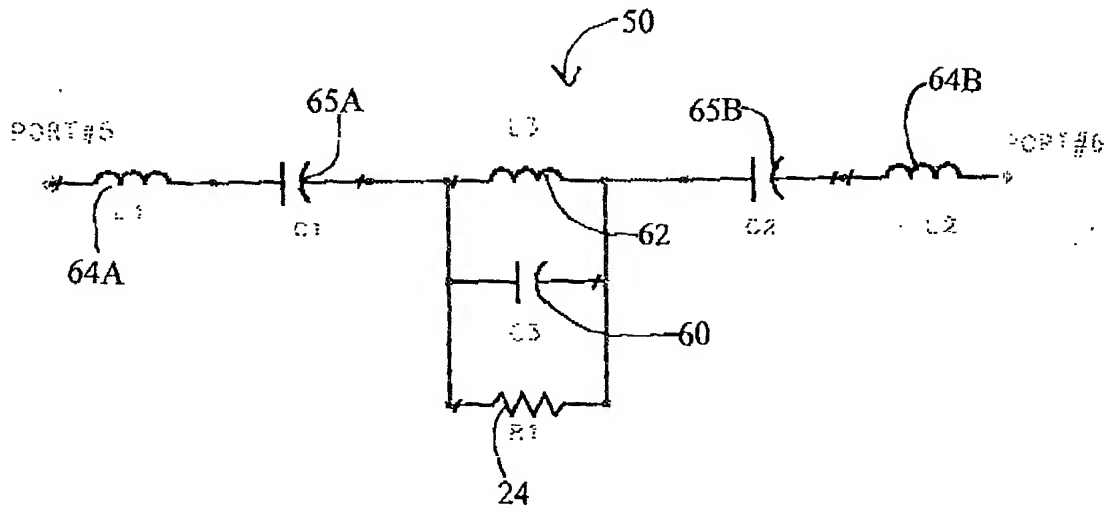


FIG. 4. CLASS S AMPLIFIER: OUTPUT FILTER AND LOAD SCHEMA [IC

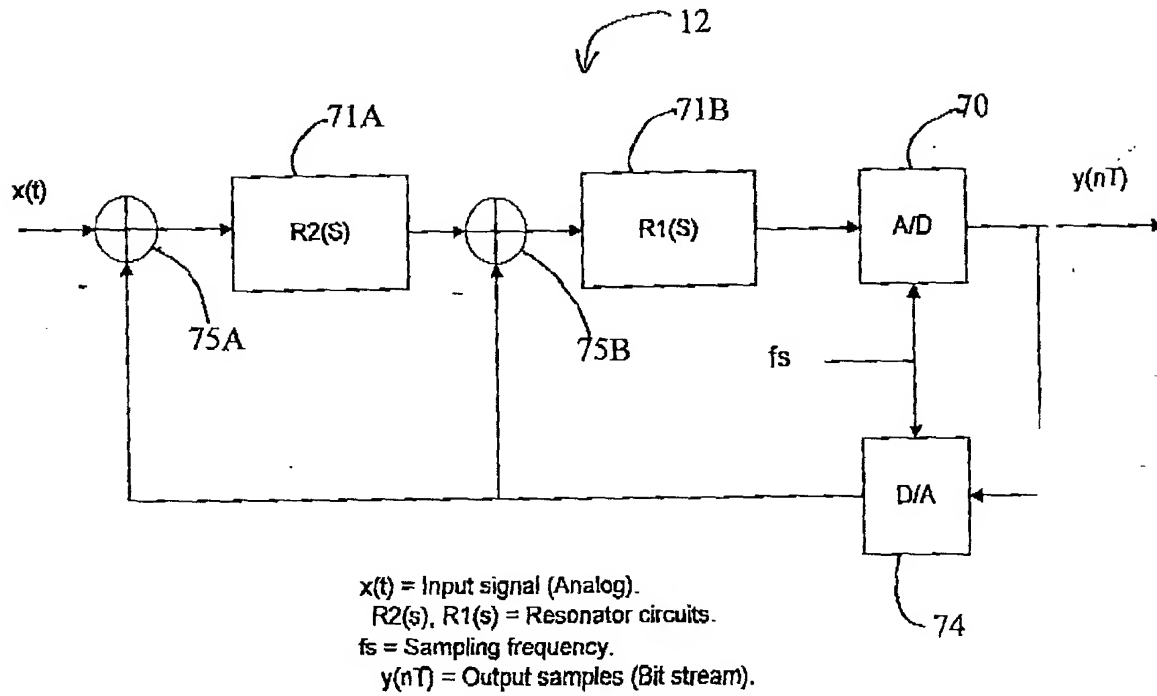
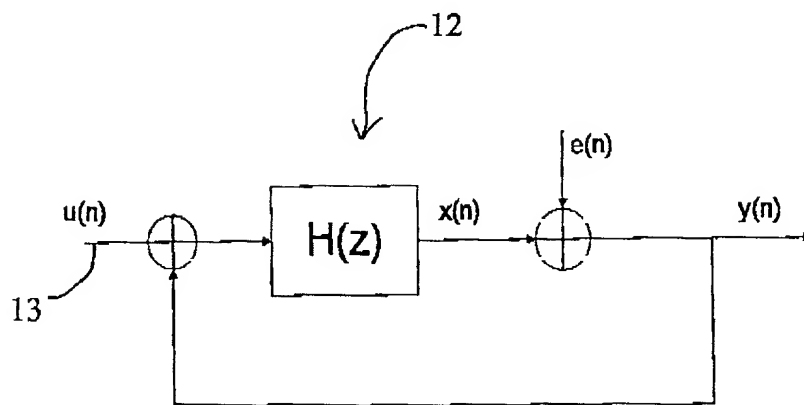


FIG. 5. CONTINUOUS TIME BANDPASS DELTA SIGMA MODULATOR USING DOUBLE FEEDBACK LOOP.



$u(n)$ = Input signal

$x(n)$ = Input to Quantizer

$e(n)$ = Quantization noise as additive noise non-correlated with input

$y(n)$ = Output bit stream

FIG. 6. SIMPLIFIED DISCRETE EQUIVALENT OF DELTA SIGMA MODULATOR.

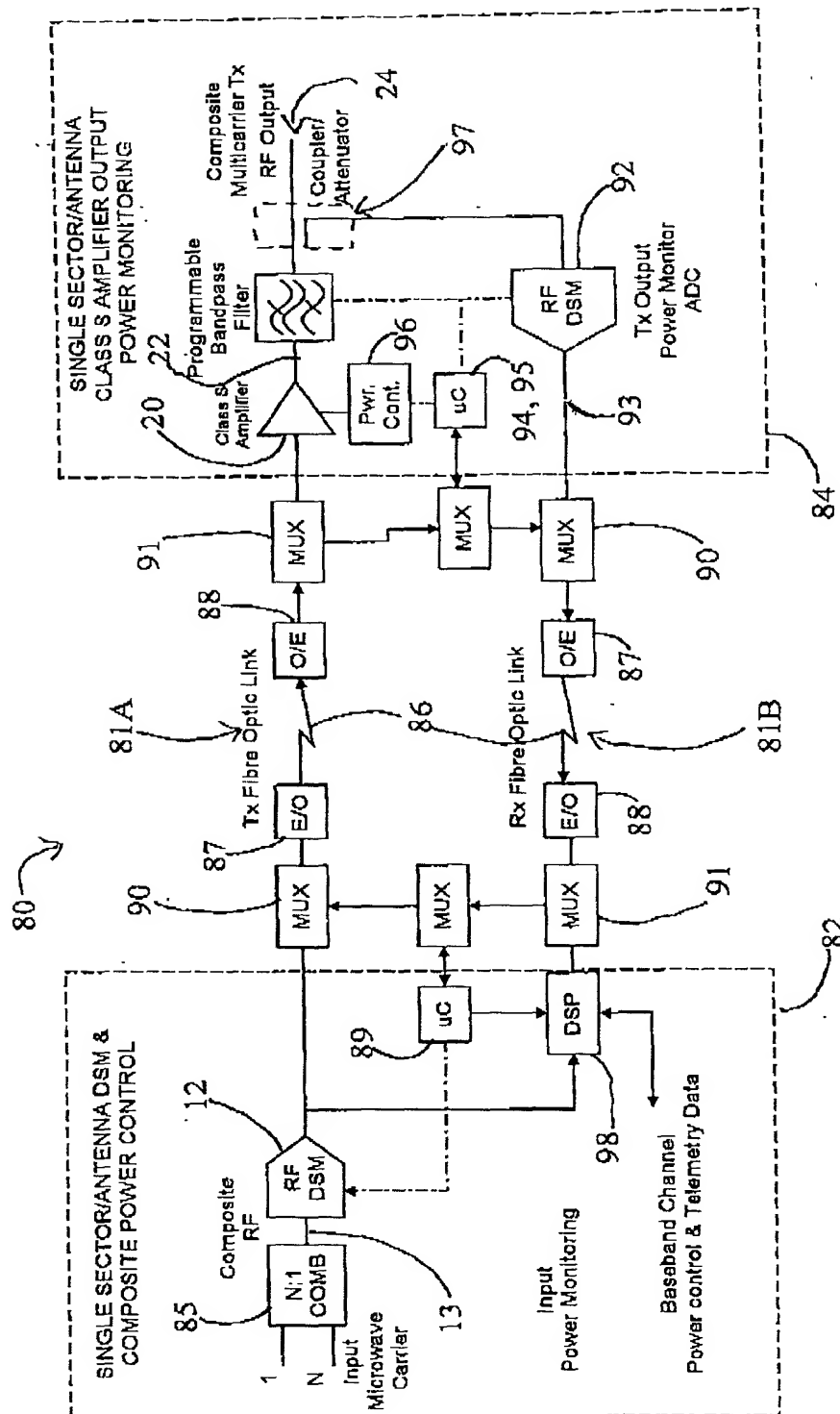


FIGURE 7A

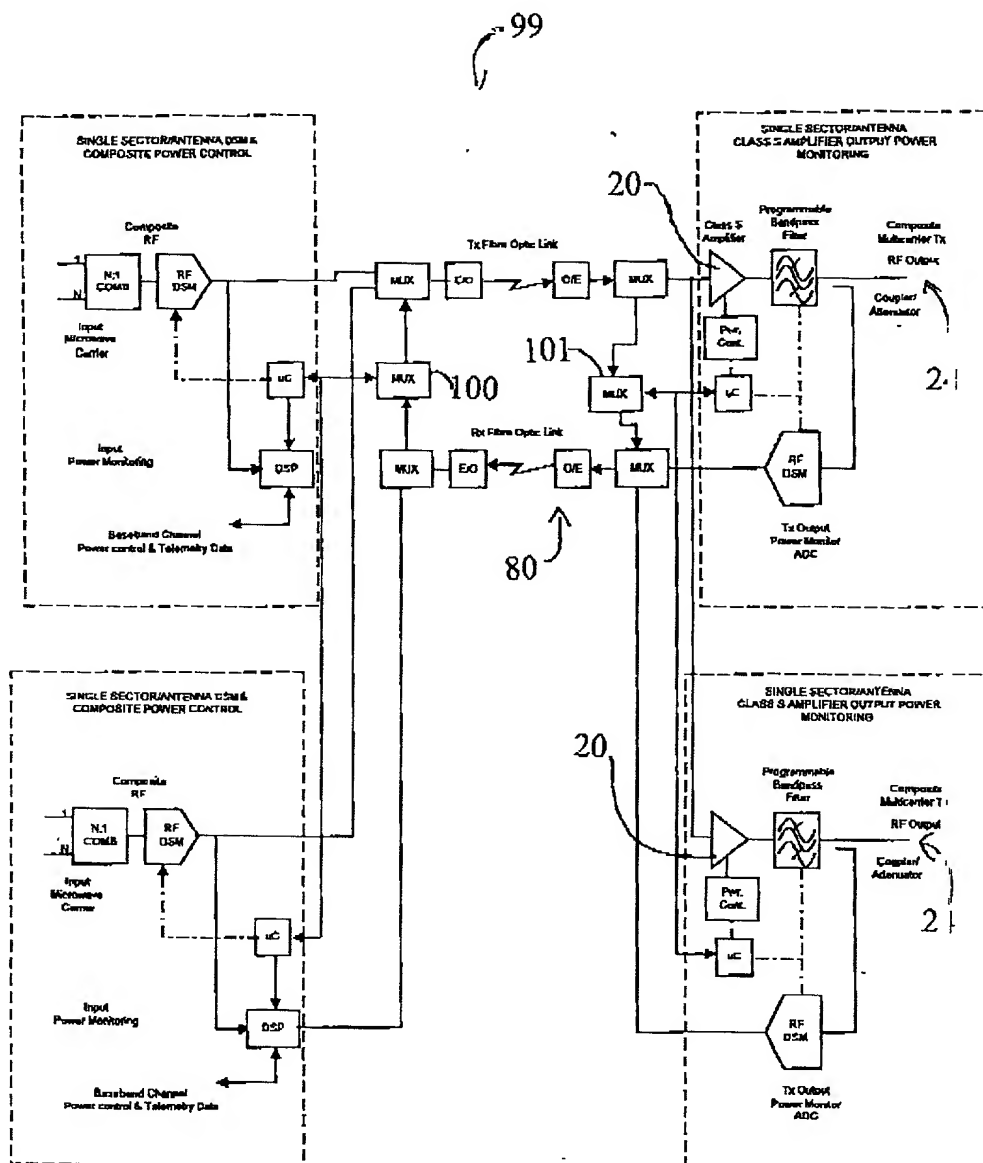


FIGURE 7B